

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT NO.:	6,854,051	GRANT DATE:	February 8, 2005
INVENTOR(S):	Shubhendu S. Mukherjee	ATTY. DOCKET NO.:	200301819-1
SERIAL NO.:	09/839,459	CONFIRMATION NO.:	2460
FILED:	April 19, 2001		
TITLE:	CYCLE COUNT REPLICATION IN A SIMULTANEOUS AND REDUNDANTLY THREADED PROCESSOR		

**REQUEST FOR ISSUANCE OF
CERTIFICATE OF CORRECTION**

**COMMISSIONER FOR PATENTS
ALEXANDRIA, VA 22313-1450**

Sir:

The assignee of the entire interest in the above-identified patent respectfully requests that a Certificate of Correction be issued in accordance with 37 CFR 1.322.

Enclosed herewith is a complete copy of Form PTO 1050 (Rev. 3-82) describing the various errors involved in the above patent. The errors that occurred under Rule 322, appear in Column and Line format.

Issuance of a Certificate of Correction to correct these errors is therefore respectfully requested.

We shall appreciate your returning to us one executed copy of this Certificate of Correction for attachment to our Letters patent.

Respectfully submitted,

By



William P. O'Meara, Esq.

Reg. No. 29,962

Hewlett-Packard Company
Legal Department
P.O. Box 272400 MS 35
Ft. Collins, CO 80528-9599
Ph. No. (970) 898-3886